

MENU

SEARCH

INDEX

1/1



JAPANESE PATENT OFFICE

PATENT ABSTRACTS OF JAPAN

(11)Publication number: 05189307

(43)Date of publication of application: 30.07.1993

(51)Int.Cl.

G06F 12/08

(21)Application number: 04005720

(71)Applicant:

FUJITSU LTD

(22)Date of filing: 16.01.1992

(72)Inventor:

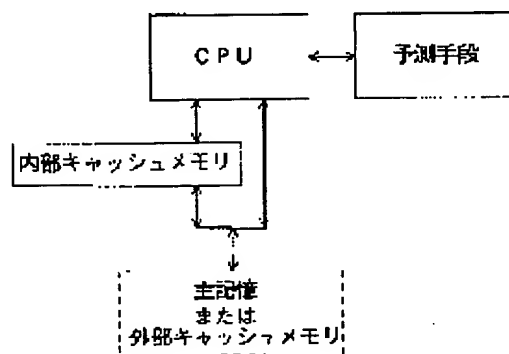
SUGA ATSUHIRO

(54) COMPUTER SYSTEM

(57)Abstract:

PURPOSE: To improve the performance of a CPU and to improve an average access time by either using a cache memory or not depending on a program.

CONSTITUTION: A computer system in which an arbitrary program is executed by transferring a command, data, and arithmetic result or the like between a CPU and an inside cache memory, is equipped with a predicting means which predicts a cache miss ratio at the time of executing the program. When the cache miss ratio exceeding a prescribed value is predicted, the command, data, and arithmetic result or the like are directly transferred between the CPU and a main storage device or an outside cache memory positioned at the higher rank than the main storage device without allowing the data to pass through the inside cache memory.



LEGAL STATUS